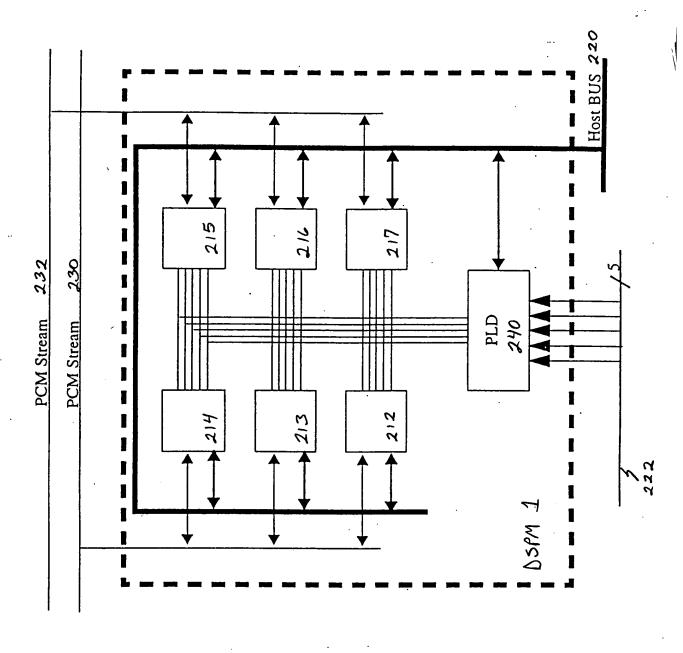


FIGURE 2



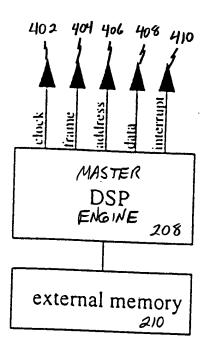


FIGURE 4

500

FIGURE 5

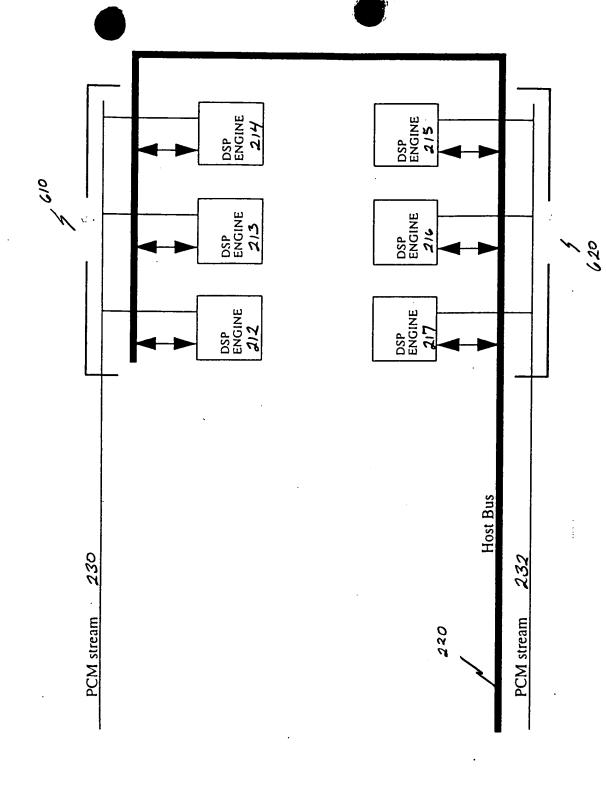
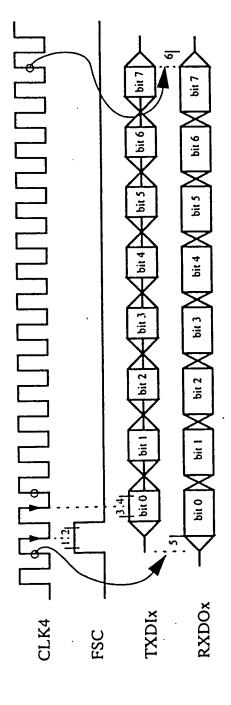


FIGURE 6



= fsc and txd input sampling active edge= rxd output active edge

FIGURE 7